



501.40201X00

AF

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Applicants: TADOKORO, et al.

Serial No.: 09/893,577

Filed: June 29, 2001

For: Fabrication Method Of Semiconductor Integrated  
Circuit Device

Art Unit: 1765

Examiner: Kin Chen

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**NOTICE OF APPEAL FROM THE PRIMARY  
EXAMINER TO THE BOARD OF APPEALS**

Mail Stop AF  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 10, 2003

Sir:

In accordance with the provisions of 37 CFR 1.191, Applicants hereby appeal from the final rejection as set forth in the Official Action mailed June 10, 2003, finally rejecting claims 1-6 and 8-40.

A Credit Card Payment form in the amount of \$330.00 is enclosed, to cover the Appeal Fee.

Respectfully submitted,

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